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** CONTINUING DATA *****
 NONE *an*

** FOREIGN APPLICATIONS *****
 NONE *an*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 12	TOTAL CLAIMS 46	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>an</i>				
Verified and Acknowledged	Examiner's Signature	Initials		

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TITLE
 Method for improving transistor performance through reducing the salicide interface resistance

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